



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Osamu NAKAMURA

Serial No.: 10/809,118

Filed: March 25, 2004

For: Semiconductor Device And  
Manufacturing Method Thereof

Examiner:

Art Unit:

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

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Shannon Wallace

Date: September 16 2004

PRELIMINARY AMENDMENT A

Prior to examination and calculation of fees, please amend the above-identified application as follows:

Adjustment Date: 05/02/2007 MTEKLEMI  
10/08/2004 CV0111 00000007 501039 10809118  
01 FC:1201 430.00 CR  
03 FC:1202 306.00 CR

05/02/2007 MTEKLEMI 00000012 501039 10809118  
01 FC:1201 344.00 DA  
02 FC:1202 270.00 DA